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1. Analyze the clocked synchronous state machine in Figure 1 which uses D flip-flops. Analyze the clocked synchronous state machine in Figure 1 which uses D flip-flops.

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Fundamentals of Logic Design: Jr. Charles H. Roth, Larry L

He is the author of Fundamentals of Logic Design, which is in its sixth edition, and Digital Systems Design using VHDL, which is in its second edition. Larry L. Kinney is a Professor and Director of Undergraduate Studies at the University of Minnesota.

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In addition to co-authoring DIGITAL SYSTEMS DESIGN USING VHDL, Dr. Roth has authored the successful FUNDAMENTALS OF LOGIC DESIGN and co-authored DIGITAL SYSTEMS DESIGN USING VERILOG. Larry L. Kinney is a Professor and Director of Undergraduate Studies at the University of Minnesota.

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